

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sommer Attorney Docket: INF-126
Filed: January 9, 2004 Examiner: TBD
Serial No.: 10/753,407 Art Unit: 2818
For: Integrated Semiconductor Memory Circuit and Method of Manufacturing the Same

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

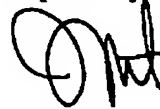
INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,



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October 20, 2004

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